

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"5854901".pn	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:29
S1	5	((multiservice adj route adj processor) (MRP)) and ((system adj processing adj engine)(SPE)) and ((CDP) (cisco adj discovery adj protocol))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:47
S2	22055	((multiservice adj route adj processor) (MRP)) and ((system adj processing adj engine)(SPE)) and ((CDP) (cisco adj discovery adj protocol)) and ((system adj switch adj processor) (SSP))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 13:54
S3	3	((multiservice adj route adj processor) (MRP)) and ((system adj processing adj engine)(SPE)) and ((CDP) (cisco adj discovery adj protocol)) and ((system adj switch adj processor) (SSP))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 13:56
S4	149	((CDP) (cisco adj discovery adj protocol)) and ((system adj switch adj processor) (SSP))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 13:57
S5	6	((system adj processing adj engine)(SPE)) and ((CDP) (cisco adj discovery adj protocol)) and ((system adj switch adj processor) (SSP))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 13:56
S6	0	((CDP) (cisco adj discovery adj protocol)) and ((system adj switch adj processor) (SSP))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 13:57
S7	4916	((CDP) (cisco adj discovery adj protocol))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:00
S8	51	((multiservice adj route adj processor) (MRP)) and ((CDP) (cisco adj discovery adj protocol))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 13:58
S9	31	((cisco adj discovery adj protocol))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:12
S10	2	"5926463".pn	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:52

S11	0	(zara) and ("20040015958")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:18
S12	0	(zara) and (09/855898)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:21
S13	0	(zara) and ("2004/0015958")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:18
S14	1	(zara) and (09/854209)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:33
S15	3	(rack) same (ethernet adj switch adj (card occupant board PCB slot))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:41
S16	0	(assign\$5) near4 (IP adj address) near5 (ethernet adj switch adj (card occupant board PCB slot))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:44
S17	0	(verify\$5 monitor\$5) near4 (IP adj address) near5 (ethernet adj switch adj (card occupant board PCB slot))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:25
S18	0	(verify\$5 monitor\$5) near4 (address) near5 (ethernet adj switch adj (card occupant board PCB slot))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:17
S19	0	(verify\$5 monitor\$5) near4 (IP adj address) near5 (ethernet adj switch adj (processor CPU card occupant board PCB slot))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:43
S20	0	(verify\$5 monitor\$5) near4 (IP adj address) near5 (ethernet adj (processor CPU card occupant board PCB slot))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:44
S21	1	(verify\$5 monitor\$5) near4 (address) near5 (ethernet adj (processor CPU card occupant board PCB slot))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:44

S22	0	(assign\$5) near4 (IP adj address) near5 (ethernet adj switch adj (processor card occupant board PCB slot))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:45
S23	2	"6415614".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 14:52
S24	2	"6415314".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/30 12:29
S25	2	"5854901".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:12
S26	0	(chasis) same (dicover\$5) near4 (IP) near4 (address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:13
S27	0	(chasis) same (discover\$5) near4 (IP) near4 (address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:13
S28	2	(chassis) same (discover\$5) near4 (IP) near4 (address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:14
S29	9	(ethernet) same (discover\$5) near4 (IP) near4 (address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:17
S30	303	(ethernet near4 switch near4 (processor card occupant board PCB slot))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:18
S31	0	(assign\$5 providing discover\$5) near6 (ethernet near4 switch near4 (processor card occupant board PCB slot)) near5 (IP)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:19
S32	10	(assign\$5 providing discover\$5) near6 (ethernet near4 switch near4 (processor card occupant board PCB slot))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:24

S33	35	(ethernet) near4 (plane) same (processor card occupant board PCB slot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:31
S34	2	(request\$5) near5 (discover\$5) near4 (ethernet) same (processor card occupant board PCB slot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:33
S35	0	(request\$5) near5 (discover\$5) near4 (data) near5 (ethernet) same (processor card occupant board PCB slot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:33
S36	4	(request\$5) near5 (discover\$5) near4 (data) near5 (processor card occupant board PCB slot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:36
S37	0	(ethernet near3 switch) near4 (IP) near4 (address) and (request\$5) near5 (discover\$5) near4 (data) near5 (processor card occupant board PCB slot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:36
S38	0	(ethernet near3 switch) near4 (IP) near4 (address) and (request\$5) near5 (discover\$5) near4 (data) near5 (processor card occupant board PCB slot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:36
S39	0	(ethernet near3 switch) near4 (IP) near4 (address) and (request\$5) near5 (discover\$5) near4 (data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:37
S40	29	(ethernet near3 switch) near4 (IP) near4 (address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:37
S41	0	((multiservice adj route adj processor)) and (system adj processing adj engine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:52
S42	0	((route adj processor)) and (system adj processing adj engine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:55
S43	13	((route near2 processor)) and (system near2 processing near2 engine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/24 15:55

S44	2	"6288716".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 09:05
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